



2813/IFW

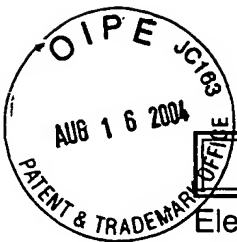
Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID: 66684
Application ID: 10773263
Title of Invention: Method for Making MOSFET
Anti-Fuse Structure
First Named Inventor: Akira ITO
Domestic/Foreign Application: Domestic Application
Filing Date: 2004-02-09
Effective Receipt Date: 2004-08-16
Submission Type: Information Disclosure
Statement
Filing Type:
Confirmation number: 8570
Attorney Docket Number: 1875.2590001




Total Fees Authorized: 180.0
Payment Category: Credit Card
Credit Card Number: *****1005
Expiration Date: 08312005
Card Holder Name: SKGF PLLC
Postal Code: 20005
RAM Payment Status: RAM success
RAM User ID: EFSPROD
RAM Accounting Date: 2004-08-16
RAM Sequence Number: 1109934

Digital Certificate Holder: cn=Jason Daniel Eisenberg,ou=Registered Attorneys,ou=Patent and
Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US
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TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Jason D. Eisenberg Registered Number: 43447</td><td>/JDE/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Jason D. Eisenberg Registered Number: 43447	/JDE/	Attorney								
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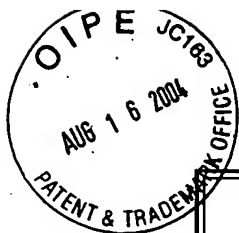


FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0


Title of Invention	Method for Making MOSFET Anti-Fuse Structure										
Application Number: 10/773263 											
Date: 2004-02-09											
First Named Applicant: Akira ITO											
Attorney Docket Number: 1875.2590001											
Art Unit: 2813											
Examiner: Jennifer M. Dolan											
TOTAL FEE AUTHORIZED \$180											
Patent fees are subject to annual revisions on or about October 1st of each year.											
BASIC FILING FEE											
<table border="1"><thead><tr><th>Fee Description</th><th>Fee Code</th><th>Amount \$</th><th>Fee Paid \$</th></tr></thead><tbody><tr><td>Submission Of Information Disclosure Stmt Fee</td><td>1806</td><td>180</td><td>180</td></tr></tbody></table>				Fee Description	Fee Code	Amount \$	Fee Paid \$	Submission Of Information Disclosure Stmt Fee	1806	180	180
Fee Description	Fee Code	Amount \$	Fee Paid \$								
Submission Of Information Disclosure Stmt Fee	1806	180	180								
AUTHORIZED BILLING INFORMATION											
The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:											
Credit account number: 1005											
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Authorized name: SKGF PLLC											
Billing address: 20005											



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Method for Making MOSFET Anti-Fuse Structure																				
<p>Application Number: 10/773263 </p> <p>Confirmation Number: 8570</p> <p>First Named Applicant: Akira ITO</p> <p>Attorney Docket Number: 1875.2590001</p> <p>Art Unit: 2813</p> <p>Examiner: Jennifer M. Dolan</p> <p>Search string: (6700176).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6700176</td><td>2004-03-02</td><td>Ito et al.</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6700176	2004-03-02	Ito et al.				Examiner Name	Date		
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